

APR 12 2004

TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
16782

In Re: Application Of: **Yoshihisa MATSUBARA, et al.**

Serial No.
10/608,647

Filing Date
June 27, 2003

Examiner
Roy Karl Potter

Group Art Unit
2822

Title: **SEMICONDUCTOR APPARATUS OF WHICH RELIABILITY OF INTERCONNECTIONS
IS IMPROVED AND MANUFACTURING METHOD OF THE SAME**

TO THE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE:

Transmitted herewith is:

Response to Requirement for Restriction

in the above identified application.

- ☒ No additional fee is required.
- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **19-1013/SSMP**
as described below.
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Signature

Dated: **April 7, 2004**

Paul J. Esatto, Jr., Reg. No. 30,749
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(516) 742-4343

PJE:ahs

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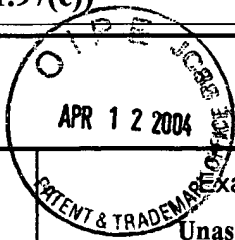
Paul J. Esatto, Jr.

Typed or Printed Name of Person Mailing Correspondence

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
14699

In Re Application Of: **Masaki ICHIHARA, et al.**



Serial No.
09/879,323

Filing Date
June 12, 2001

Examiner
Unassigned

Group Art Unit
2631

Title: **ORTHOGONAL FREQUENCY DIVISION MULTIPLEX MODEM CIRCUIT**

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

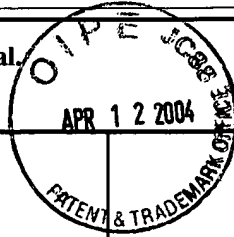
37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
- ☐ the statement specified in 37 CFR 1.97(e);
- OR**
- ☐ the fee set forth in 37 CFR 1.17(p).

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
14699

In Re Application: Masaki ICHIHARA, et al.



Serial No.
09/879,323

Filing Date
June 12, 2001

Examiner
Unassigned

Group Art Unit
2631

ORTHOGONAL FREQUENCY DIVISION MULTIPLEX MODEM CIRCUIT

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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- ☒ The Director is hereby authorized to charge and credit Deposit Account **19-1013/SSMP** as described below.
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Paul J. Esatto, Jr.

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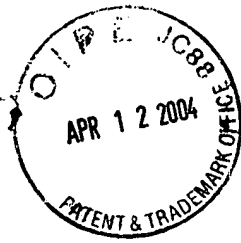
Signature

Dated: April 7, 2004

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CC:



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yoshihisa MATSUBARA, et al.

Examiner: Roy Karl Potter

Serial No.: 10/608,647

Group Art Unit: 2822

Filed: June 27, 2003

Docket: 16782

For: SEMICONDUCTOR APPARATUS
OF WHICH RELIABILITY OF
INTERCONNECTIONS IS IMPROVED
AND MANUFACTURING METHOD OF
THE SAME

Confirm. No. 8331

Dated: April 7, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO REQUIREMENT FOR RESTRICTION

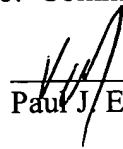
Sir:

Pursuant to the Restriction Requirement imposed in the Official Action dated March 12, 2004, applicants provisionally elect the claims of Group I, i.e., Claims 1 to 7, for continued prosecution herein.

Claims 1-22 are present in the above-captioned application and have been subjected to restriction under 35 U.S.C. §121. Specifically, the Official Action avers that the following inventions are present in the claims:

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

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Dated: April 7, 2004


Paul J. Esatto, Jr.

Group I, Claims 1-7, drawn to a semiconductor apparatus, classified in Class 257,
Subclass 758; and

Group II, Claims 8-22, drawn to a manufacturing method of a semiconductor
apparatus, classified in Class 438, Subclass 118.

It is the Examiner's position that the inventions listed as Groups I and II are
distinct from each other.

In response to the Examiner's requirement for restriction, applicants provisionally
elect to prosecute the subject matter of Group I, Claims 1 to 7. However, applicants reserve the right
under 35 U.S.C. §121 to file one or more divisional applications directed to the non-elected claims in
this application.

In view of the foregoing, an examination on the merits of the elected claims, at an
early date, is earnestly solicited.

Respectfully submitted,



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